

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sohn et al. Docket: 8021-225 (SS-18860-US)

Serial No. 10/814,968 Group Art Unit: 2185

Filed: March 31, 2004 Examiner: Campos, Yaima

For: **INTEGRATED CIRCUIT HAVING A MEMORY CELL
ARRAY CAPABLE OF SIMULTANEOUSLY PERFORMING
A DATA READ OPERATION AND A DATA WRITE OPERATION**

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria VA 22311-1450

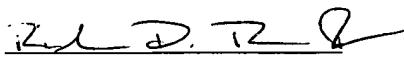
SUBMISSION OF TRANSLATION

Sir:

Submitted herewith is a translation of the Korean Patent Application No. 10-2003-0023733 filed on April 15, 2003 and a statement that the translation is accurate.

Respectfully submitted,

By:

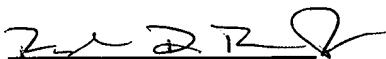

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CERTIFICATION OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria VA 22311-1450 on July 21, 2006.

Dated: July 21, 2006


Richard D. Ratchford, Jr.

CERTIFICATION OF TRANSLATION

I, Jungkum Lee, an employee of Y.P. LEE, MOCK & PARTNERS of The Koryo Building, 1575-1 Seocho-dong, Seocho-gu, Seoul, Republic of Korea 137-875, hereby declare under penalty of perjury that I understand the Korean language and the English language; that I am fully capable of translating from Korean to English and vice versa; and that, to the best of my knowledge and belief, the statement in the English language in the attached translation of Korean Patent Application No. 10-2003-0023733 consisting of 26 pages, have the same meanings as the statements in the Korean language in the original document, a copy of which I have examined.

Signed this 21st day of July 2006

Leejungkum